

74xx Logic Series IC Functions by Number

No	Description	No	Description					
00	Quad 2-Input NAND Gate	01	Quad 2-Input NAND Gate (OC)					
02	Quad 2-Input Positive NOR Gate	03	Quad 2-Input Positive NAND Gate					
04	Hex Inverter		Hex Inverter (OC)					
06	Hex Inverter Buffer/Driver (OC)	07	30V,40mA Hex Buffer Driver (OC)					
_	Quad 2-Input AND Gate		Quad 2-Input Positive AND Gate (OC)					
_	Triple 3-Input Positive NAND Gate		Triple 3-Input Positive AND Gate					
	Hex Inverting Schmitt Trigger		Hex Inverter Buffer/Driver (OC)					
	15V,40mA Hex Buffer Driver (OC)	_	Dual 4-Input Positive NAND Gate					
_	Dual 4-Input Positive AND Gate		Dual 4-Input Positive NOR Gate					
_	Quad 2-Input NAND Gate (OC)		Triple 3-Input NOR Gate					
	Quad 2-Input NOR Buffer		8-Input Positive NAND Gate					
	Delay Element		Quad 2-Input Positive OR Gate					
	Quad 2-Input NOR Buffer (OC)		Quad 2-Input Positive NAND Buffer					
	Quad 2-Input NAND Buffer (OC)		BCD To Decimal Decoder (1-of-10)					
_	BCD To Decimal Decoder/Driver (OC)		BCD To 7 Segment Decoder/Driver (30V)					
_	BCD To 7 Segment Decoder/Driver (15V)		BCD To 7 Segment Decoder/Driver					
	Dual 2 Wide 2-Input AND-OR Invert Gate		Dual AND-OR Gate					
	Dual JK Flip-Flop with Reset; Negative-Edge Trigger		Dual D-Type Flip-Flop with Set and Reset; Positive-Edge Trigger					
	Quad Bistable Transparent Latch 4 Dit Bistable Latch		Dual JK Master Slave Flip-Flop					
_	4-Bit Bistable Latch		4-Bit Binary Full Adder					
	4-Bit Magnitude Comparator		Quad 2-Input EXCLUSIVE-OR Gate					
	Decade Counter	=	4-Bit Binary Ripple Counter					
_	6-Bit Asynchronous Binary Rate Multiplier		Dual JK Flip-Flop with Reset; Negative-Edge Trigger					
	Dual JK Flip-Flop with Set and Reset; Positive-Edge Trigger		Dual JK Flip-Flop with Set and Reset; Negative-Edge Trigger					
	One Short Multivibrator		Retriggerable Monostable Multivibrator					
_	Dual Retriggerable Monostable Multivibrator with Reset		Dual Voltage Controlled Oscillator					
	Quad Buffer/Line Driver (3-State)		Quad Buffer/Line Driver (3-State)					
_	Quad 2-Input NOR Line Driver		Quad 2-Input NAND Schmitt Trigger					
	13-Input NAND Gate		Quad Exclusive OR Gate (OC)					
	3-to-8 Line Decoder/Demultiplexer with Address Latches; Inv Dual 2-to-4 Line Decoder/Demultiplexer		3-to-8 Line Decoder/Demultiplexer; Inverting Dual 4-Input NAND Line Driver					
	BCD To Decimal Decoder/Driver (OC)		10-to-4 Line Priority Encoder					
	8-to-3 Priority Encoder	_	16-Input Multiplexer					
_	8-Input Multiplexer		Dual 4-Input Multiplexer					
	4-to-16 Line Decoder/Demultiplexer		Dual 2-to-4 Demultiplexer					
	Dual 2-to-4 Demultiplexer (OC)		Quad 2-Input Multiplexer					
	Quad 2-Input Multiplexer; Inverting		4-to-16 Line Decoder					
	Presettable Synchronous BCD Decade Counter; Reset		Presettable Synchronous 4-Bit Binary Counter; Reset					
	Presettable Synchronous 4-Bit Binary Counter; Reset		8-Bit Serial-In/Parallel-Out Shift Register					
	8-Bit Parallel-In/Serial-Out Shift Register		8-Bit Parallel-In/Serial-Out Shift Register					
_	Synchronous 4-Bit Binary Up/Down Counter		Quad D-Type Flip-Flop; Positive-Edge Trigger (3-State)					
	Hex D-Type Flip-Flop with Reset; Positive-Edge Trigger		Quad D-Type Flip-Flop with Reset; Positive-Edge Trigger					
	64-Bit Bipolar Scratch Pad Memory	_	Synchronous Decade Up/Down Counter					
	Presettable Synchronous 4-Bit Binary Up/Down Counter		Presettable Synchronous 4-Bit Binary Up/Down Counter					
	4-Bit Bidirectional Universal Shift Register		4-Bit Parallel Access Shift Register					
_			3-to-8 Line Decoder/Demultiplexer with Address Latches					
_			Octal Buffer/Line Driver; Inverting (3-State)					
			Quad Bus Transceiver (3-State)					
_			Octal Bus Transceiver (3-State)					
_			Dual 4-Input Multiplexer (3-State)					
			Quad 2-Input Multiplexer; Inverting (3-State)					
			Dual 5-Bit Input NOR Gate					
269	8-Bit Up/Down Counter	273	Octal D-Type Flip-Flop with Reset; Positive-Edge Trigger					
_			9-Bit Odd/Even Parity Generator/Checker					
283	4-Bit Binary Full Adder with Fast Carry		16-Bit Programmable Frequency Divider					
297	Digital Phase Locked Loop	299	8-Bit Universal Shift Register (3-State)					
321	Crystal Oscillator	365	Hex Buffer/Line Driver (3-State)					
366	Hex Buffer/Line Driver; Inverting (3-State)	367	Hex Buffer/Line Driver (3-State)					
368	Hex Buffer/Line Driver; Inverting (3-State)	373	Octal D-Type Transparent Latch (3-State)					

274	Out ID Town Flim Flow Positives Edge Trigger (2 State)	275	O-H-wh					
	- JF F F		Quad Latch Hay D. Type Flip Flop with Enable					
	Jr r r r was an experience and acceptance and accep	_	Hex D-Type Flip-Flop with Enable Dual 4-Bit Binary Ripple Counter					
390			Dual Retriggerable Monostable Multivibrator with Reset					
			Octal Comparator					
			Octal Buffer/Line Driver; Inverting (3-State)					
	Octal Buffer/Line Driver (3-State)	=	Octal Registered Transceiver					
_			Octal D-Type Flip-Flop; Positive-Edge Trigger; Inverting (3-State)					
		_	Octal D-Type Transparent Latch (3-State)					
			8-Bit Up/Down Counter					
	7F F F 7F 7 2 2 2 2 2 2 2 2 2 2 2 2 2 2							
_	7F	_	8-Bit Shift Register with Input Latch					
	e zie ziminy e e miner with imp we reegater		8-Bit Binary Counter O/P Register TS I/O 8 Dit Son In /Son On Pan Out Shift Provident with Out Lotales (2 State)					
	2 2 10 211110 1 10 Bisson 11 1111 0 the part 1 10 Bisson		8-Bit Ser-In/Ser Or Par-Out Shift Register with Out Latches (3-State)					
	The Part of the Pa		16 to 8 Multiplexer (High Speed)					
			Voltage Controlled Oscillator					
			Octal Bus Transceiver; Inverting (3-State)					
_	2.11		Octal Bus Transceiver					
	2 - 111 - 112 - 111 - 112		Octal Bus Transceiver/Register (3-State)					
		_	16-Bit Shift Register 16-Bit Parallel Output					
	The state of the s	_	8-Bit Magnitude Comparator					
			10-Bit D-Type Flip-Flop					
			8-Bit Buffer/Line Driver					
	The state of the s		9-Bit Transparent Latch					
		_	Open Drain Buffer (Active Pull Down)					
	=8		Display Controller 6 Digit 8 Segment					
914	Hex Schmitt Trigger External Voltage Inputs	922	16 Key Keyboard Encoder					
923	20 Key Keyboard Encoder	925	4 Digit Counter Multiplexed 7 Segment Drive					
2244	Octal Tri-State Buffer with Resistor	2245	Octal Tri-State Transceiver with Resistor					
3245	Octal Tri-State Translating Transceiver	3257	Quad 2:1 Mux/Demux Bus Switch					
3383	Octal Bus Exchange Switch	3384	10-Bit Bus Switch					
4002	Dual 4-Input NOR Gate	4015	Dual 4-Bit Serial-In/Parallel-Out Shift Register					
4016	Quad Bilateral Switches	4017	Johnson Decade Counter with 10 Decoded Outputs					
4020	14-Stage Binary Ripple Counter	4024	7-Stage Binary Ripple Counter					
4040	12-Stage Binary Ripple Counter	4046	Phase Locked Loop					
4049	Hex Inverting High-to-Low Level Shifter	4050	Hex High-to-Low Level Shifter					
4051	8-Channel Analog Multiplexer/Demultiplexer	4052	Dual 4-Channel Analog Multiplexer/Demultiplexer					
4053	Triple 2-Channel Analog Multiplexer/Demultiplexer	4059	Programmable Divide-By-N Counter					
	14-Stage Binary Ripple Counter with Oscillator	4066	Quad Bilateral Switches					
		4075	Triple 3-Input OR Gate					
		_	8-Stage Shift-and-Store Bus Register					
_	•		Quad Bilateral Switches					
			Triple 2-Channel Analog Multiplexer/Demultiplexer with Latch					
			4-to-16 Line Decoder/Demultiplexer with Input Latches					
	4-to-16 Line Decoder/Demultiplexer with Input Latches; Inverting							
		_	Dual Retriggerable Precision Monostable Multivibrator					
		=						
	Hex Non-Inverting Precision Schmitt-Trigger		9-Bit x 64-Word FIFO Register (3-State)					
	Phase-Locked-Loop with Lock Detector		Quad 2-Input EXCLUSIVE-NOR Gate					
	4-Bit x 64-Word FIFO Register (3-State)		Octal Schmitt Trigger Buffer/Line Driver; Inverting (3-State)					
			Quad 64-Bit Static Shift Register					
			Nine Wide Schmitt Trigger Buffer; Open Drain Outputs					
			8x8 Crosspoint Switch					
			os 4-Rit v 16-Word FIFO Register					

TTL Families

40105 4-Bit x 16-Word FIFO Register

8-Bit Synchronous Binary Down Counter

Family	Description	Family	Description
74	True TTL	74L	Low power
74S	Schottky	74H	High speed
74LS	Low power - Schottky	74AS	Advanced - Schottky
74ALS	Advanced - Low power - Schottky	74F(AST)	Fast - (Advanced - Schottky)
74C	CMOScheck Vcc levels	74HC (U)	High speed - CMOS (Unbuffered output)
74НСТ	High speed - CMOS - TTL inputs	74AHC	Advanced - High speed - CMOS
74АНСТ	Advanced - High speed - CMOS - TTL inputs	74FCT (-A,-T,-AT)	Fast - CMOS - TTL inputs (speed variations)
74AC	Advanced - CMOS	74ACT	Advanced - CMOS - TTL inputs
74FACT	AC, ACT (Q) series	74ACQ	Advanced - CMOS - Quiet outputs
74ACTQ	Advanced - CMOS - TTL inputs - Quiet outputs		

Bus Driver Families

Family	Description	Family	Description
74ABT	Advanced - BiCMOS - Technology	74ABTE	ABT - Enhanced Transceiver Logic

	74BTL		- Transce	iver - Logic	gy - bus Hol (BiCMOS)	d	74BCT 74FB 74GTLP	Futur	MOS - TTL inpurebus+ - Plus	ıts			
	Family			Description	n	Low V	oltage/ Famil	Familie v	es	Des	cription		
	74ALB 74LVC (R) (U) 74ALVC 74LVTZ 74ALVCH	LV - CMC Advanced Low - Volt Advanced	OS (dampin - Low - V tage - TTL - Low - V	Itage - BiCM ng Resistor)(oltage - CM High Impe oltage - CM	1OS Unbuffered (er-up ld	74LV (U) 74LVCH 74LVT (R) (U 74ALVC (R) 74LCX	Low Low LV - ALV CMC	- Voltage (Unb - Voltage - CM TTL (damping OS (bus Hold) (d CMOS (operat	uffered out [OS - bus H Resistor)(U damping Re	put) (CMOS old Jnbuffered o esistor)	utput) (BiCM	OS)
	Technolog	an. T	ivno	11	Standa On Sem		ic Com	petitive Philips	e Position Toshiba	IDT	Dor	iaam Uii	tachi
	Bipolar	LS/S/T ALS/A		164 189	81	176 268		17	TOSTIIDA	<u>IDI</u>	rei	<u>icom</u> <u>Hit</u> 124	idelli
		ECL CD400 HC/HC		39 200	336 81 68	42 51 77	90	0 75	30 140			88 191	
		AC/AC AHC/\	CT/FACT 1 VHC 4	189 12	88 53	189 57	5:	5	83 63			73	
	CMOS	LV FCT LVC/L	ç	13 97 32	27 27	53	7:		70	123 127	147 25	48	
		ALVC AVC Bus Sw	2	106 20 67	16	48	34 11		42	111	50 108	56 5	
	BICMOS	BCT ABT	2	26 90		33		06		113	108	17	
		LVT ALVT		54 13		49	3:				6		
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